

iCore3_Nano扩展引脚注释图 V1.0

注释

- 绿色为GND脚
- 红色为电源脚
- 黄色为ARM扩展脚
- 紫色为FPGA扩展脚
- 蓝色为其他引脚

复用关系表

序号	对应名字	对应引脚	连接芯片
1	ARM KEY	PH15	ARM
2	ARM LEDR	PI5	ARM
3	ARM LEDG	PI6	ARM
4	ARM LEDB	PI7	ARM
5	FPGA KEY	C2	FPGA
6	FPGA LEDH	F14	FPGA
7	FPGA LEDI	F13	FPGA
8	FPGA LEDJ	E11	FPGA

J10引脚列表			
引脚编号	第一列	第二列	引脚编号
1	D3V3	A2V5	2
3	GND	TCK	4
5	ARM_RST	TDO	6
7	GND	TMS	8
9	SWCLK	TDI	10
11	SWDIO	GND	12
13	GND	VUSBA	14
15	GND	CH340D-	16
17	GND	CH340D+	18
19	GND	GND	20
21	75R	VUSBB	22
23	75R	HSID	24
25	TX-	HSD+	26
27	TX+	HSD-	28
29	GROUND	GND	30
31	RX+	GND	32
33	RX-	GND	34
35	GROUND	D3V3	36
37	LINKLED	D3V3	38
39	ACTLED	D3V3	40

序号	第一列	第二列
1	GND	VCC
2	GND	VCC
3	GND	D3V3
4	GND	D3V3
5	GND	D3V3
6	GND	GND
7	PH7	PB14
8	PH8	PF13
9	PH9	PB2
10	PH11	PG1
11	GND	GND
12	PH10	PG0
13	PH14	PF12
14	PH13	PF11
15	ARM KEY	PF15
16	GND	GND
17	PI3	PF14
18	PA9	PH6
19	PI0	PB15
20	PA10	PH12
21	GND	GND
22	PI1	PC6
23	PA11	PG8
24	PI2	PA8
25	PA12	PC7
26	GND	GND
27	D6	PG3
28	E6	B1
29	D5	FPGA KEY
30	A3	D4
31	GND	GND
32	B3	D3
33	A2	E5
34	C3	D1
35	F5	CLK 25M
36	GND	GND
37	J6	F2
38	J1	F1
39	K2	F3
40	K1	G5
41	GND	GND
42	L2	G2
43	L1	G1
44	K5	J2
45	K6	L6
46	GND	GND
47	K8	L4
48	L7	L3
49	M6	N3
50	M7	P3
51	GND	GND
52	M2 INPUT	N5
53	M1 INPUT	N6
54	N2	P6
55	N1	P8
56	GND	GND
57	P2	P1
58	T2	R1
59	T3	R3
60	GND	GND

J10方向示意

39	1
40	2

第一列	第二列	序号
GND	VIN	1
GND	VIN	2
GND	VIN	3
CLK 12M	CLK 24M	4
GND	GND	5
PF4	VREF	6
PH5	PA6	7
PF1	PA2	8
PH3	PA4	9
GND	GND	10
PH2	PC2	11
PI10	PF10	12
P14	PF9	13
PI9	PF3	14
GND	GND	15
ARM LEDG	PH1	16
ARM LEDR	PF2	17
PE1	PFO	18
PB9	PC1	19
GND	GND	20
PE0	PF8	21
PG11	PC3	22
PB8	P18	23
PB6	PC13	24
GND	GND	25
PG12	ARM LEDB	26
PG9	A13	27
PG10	B13	28
A14	D16	29
GND	GND	30
B14	E16 INPUT	31
A15	F15	32
B16	F16	33
C15	G15	34
GND	GND	35
C16	G16	36
D15	D12	37
C14	F11	38
D14	F10	39
GND	GND	40
FPGA LEDB	F6	41
FPGA LEDR	K10	42
FPGA LEDG	K11	43
G11	K12	44
GND	GND	45
J11	L11	46
J16	L16	47
J15	L15	48
K16	M16 INPUT	49
GND	GND	50
K15	M15 INPUT	51
J12	N16	52
J14	N15	53
J13	P16	54
GND	GND	55
L13	R16	56
L14	T15	57
L12	P15	58
T14	R14	59
GND	GND	60