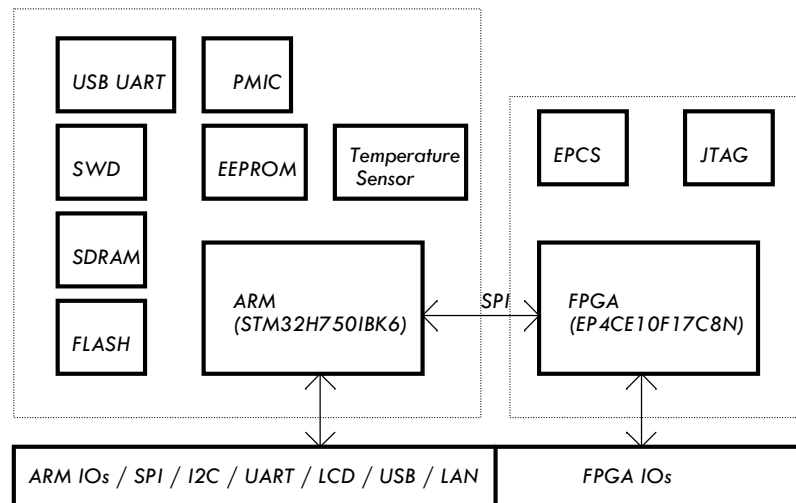


Block Diagram

iCore4 Tiny Board (iCore 4T)



百科: <http://gkwiki.cn/doku.php?id=icore4t>

预定链接: <https://item.taobao.com/item.htm?id=610595120319>

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NOTES:

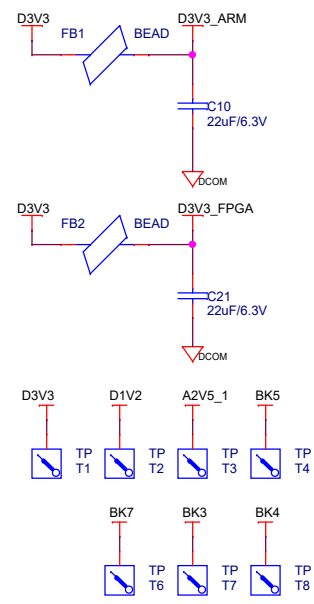
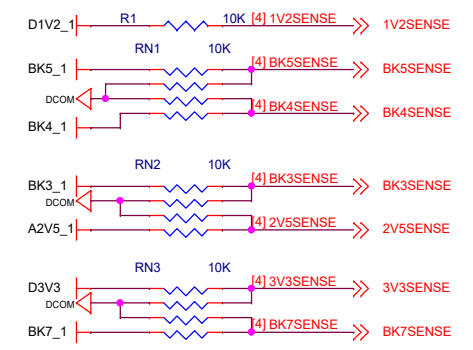
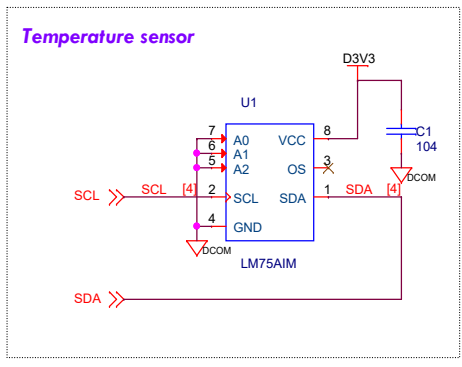
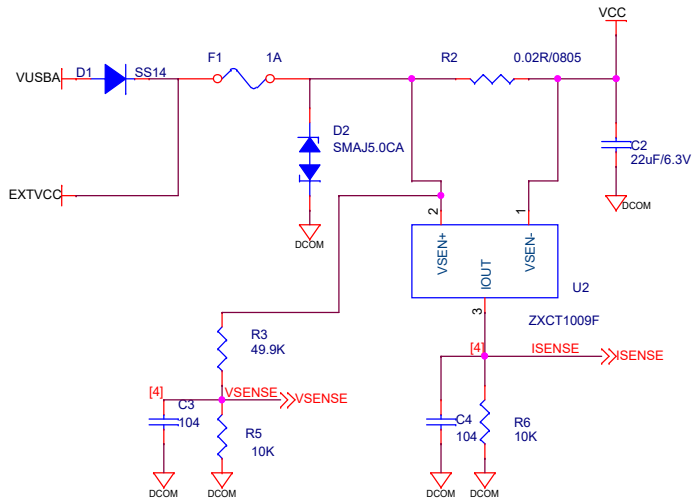
REV	DATE	PAGES	DESCRIPTION
0	01/12/2018	ALL	Created

PAGES	DESCRIPTION
0	Title, Note, Block Diagram, Revision History
1	POWER
2	CLOCK
3	STM32
4	SDRAM
5	USB
6	ARM_LAN,FPGA_LAN
7	FPGA
8	DDR2
9	CONNECTOR
A	-----
B	-----
C	-----
D	-----
E	-----
F	-----
10	-----
11	-----
12	-----
13	-----

Engineer: -----

Engineer: -----

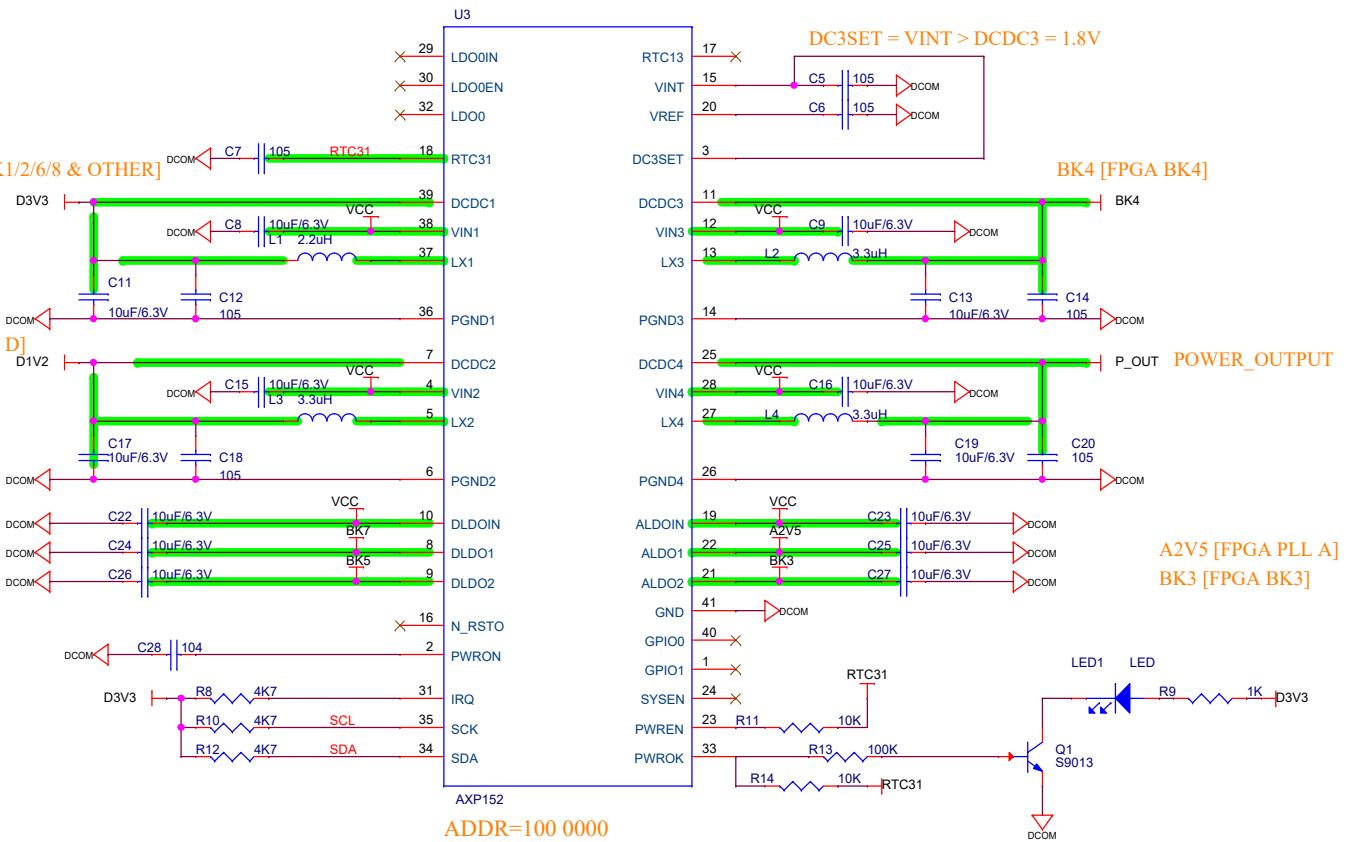
GINGKO Technology. Co., Ltd.
 Title
iCore4T.REV0.20191207
 Size B Document Number <0> Rev 0
 Date: Monday, January 13, 2020 Sheet 1 of 9



D3V3 [ARM & FPGA BK1/2/6/8 & OTHER]

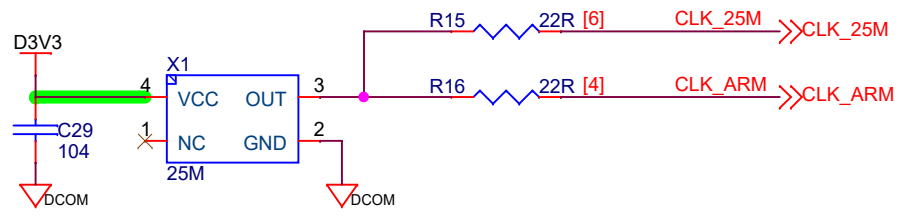
D1V2 [FPGA INT & PLL D]

BK7 [FPGA BK7]
BK5 [FPGA BK5]



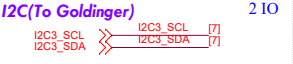
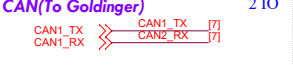
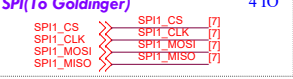
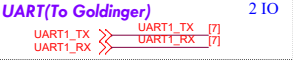
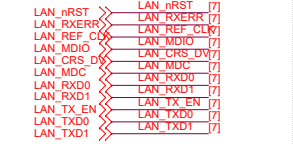
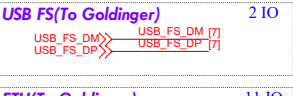
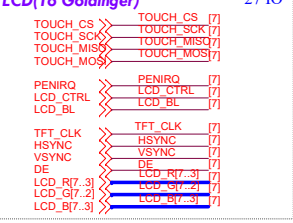
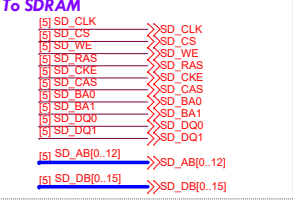
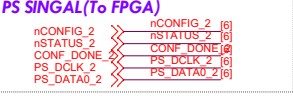
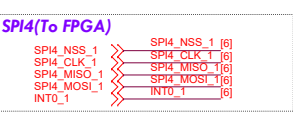
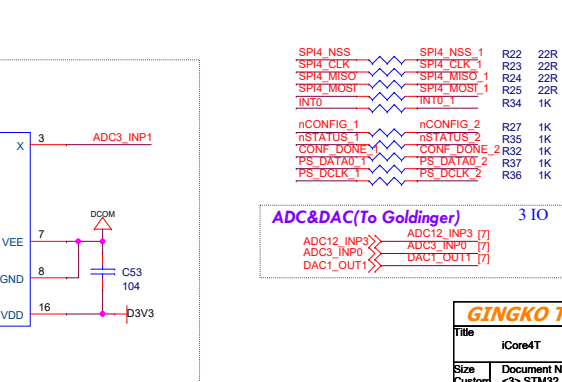
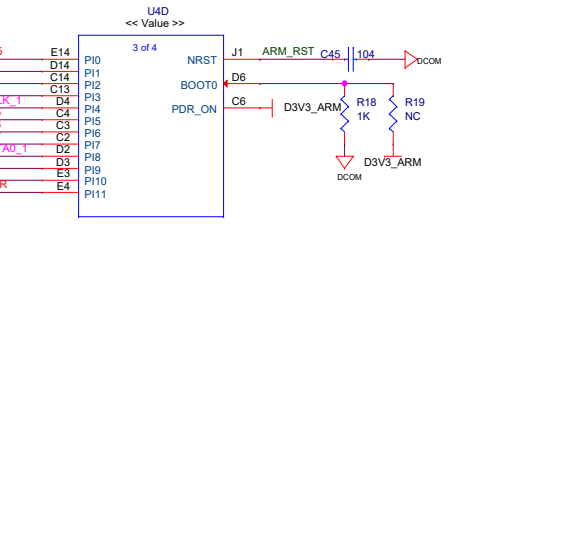
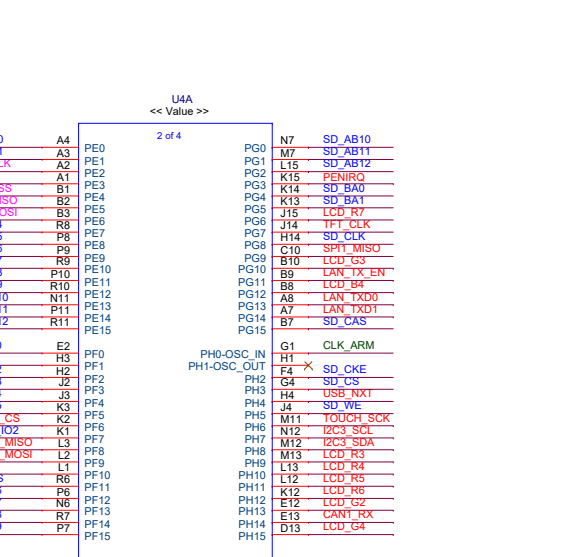
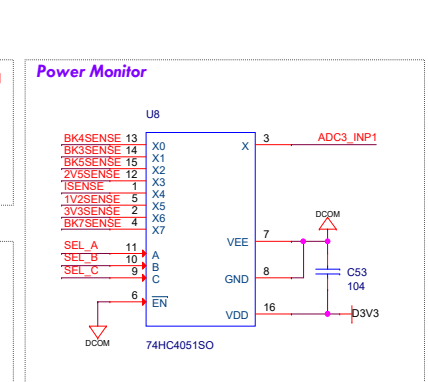
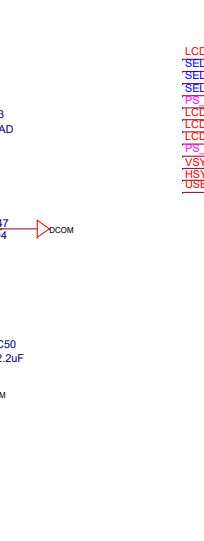
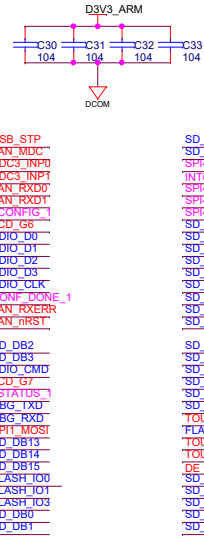
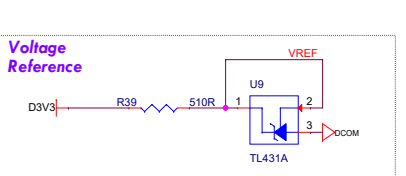
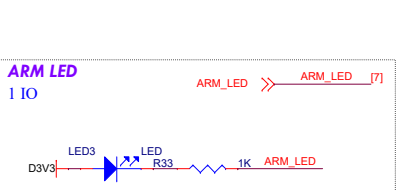
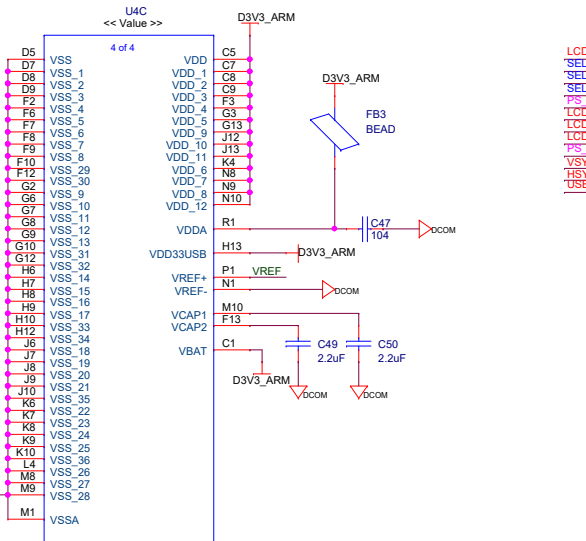
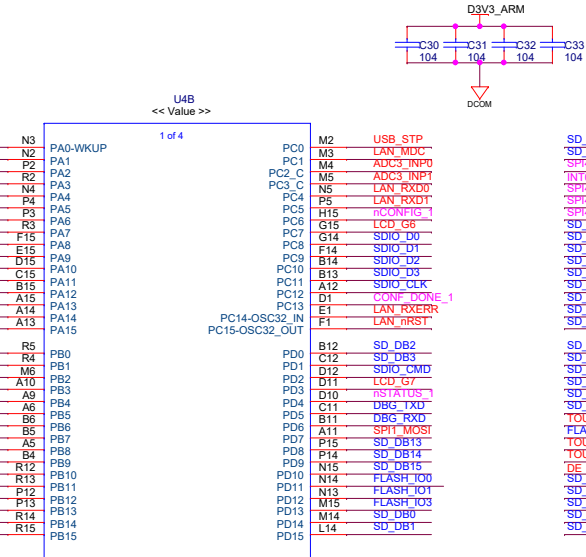
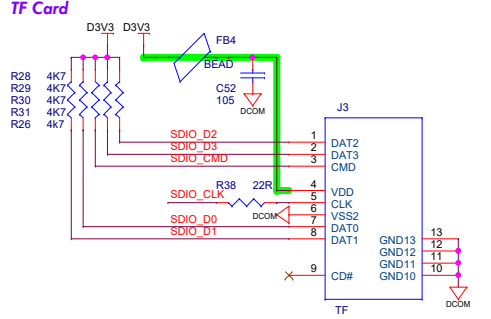
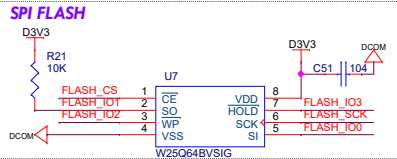
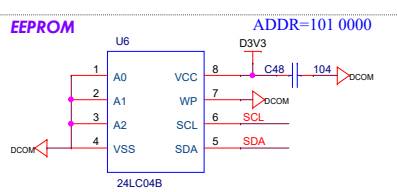
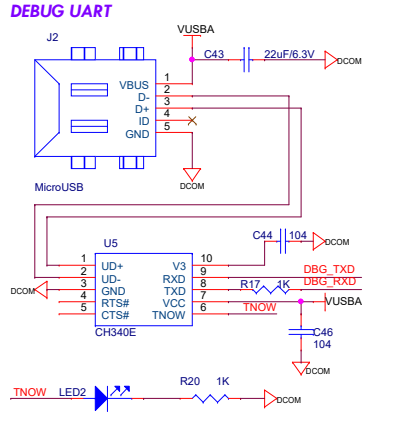
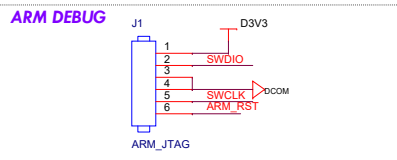
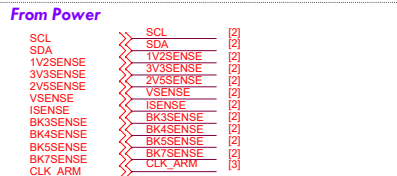
ADDR=100 0000

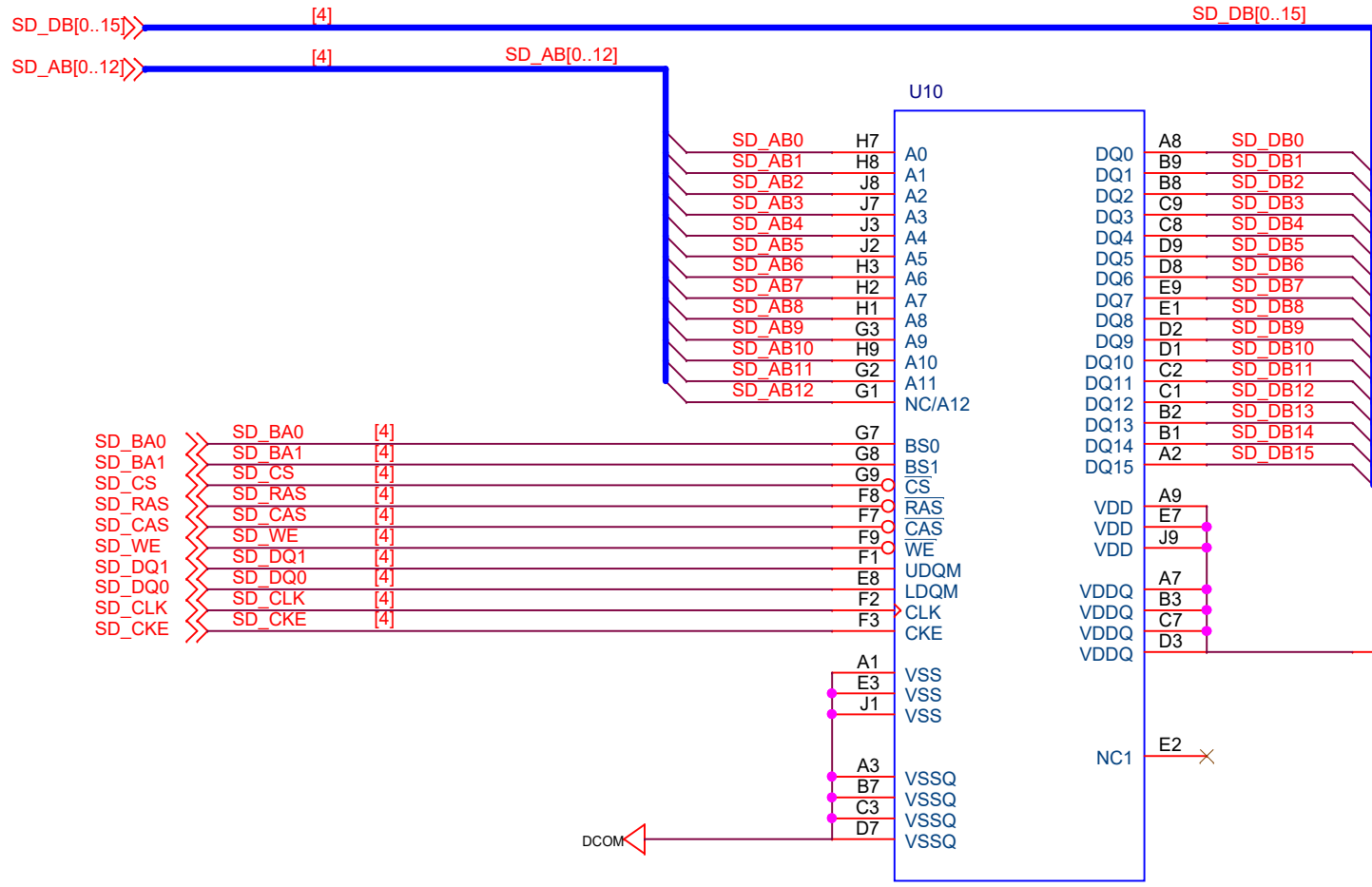
GINGKO Technology. Co., Ltd.		
Title	iCore4T	
Size	Document Number	Rev
B	<1>POWER	0
Date:	Monday, January 13, 2020	Sheet 2 of 9



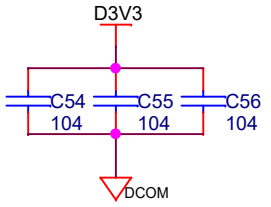
GINGKO Technology. Co., Ltd.

Title		
iCore4T		
Size	Document Number	Rev
A	<2>.CLOCK	0
Date:	Monday, January 13, 2020	Sheet 3 of 9

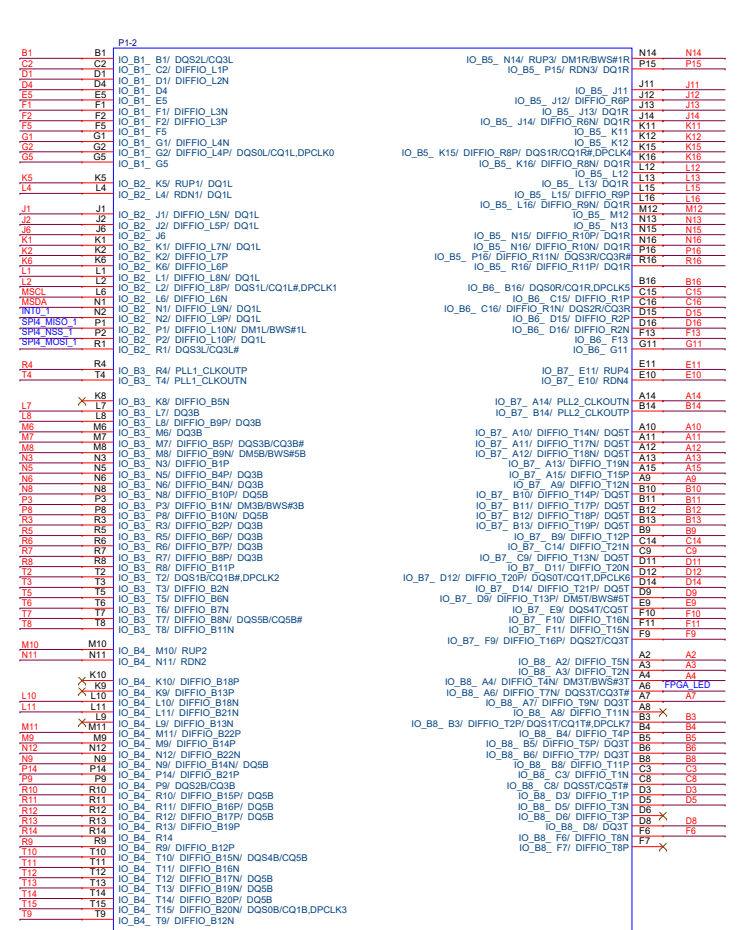
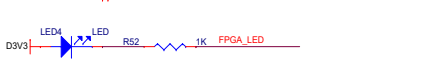
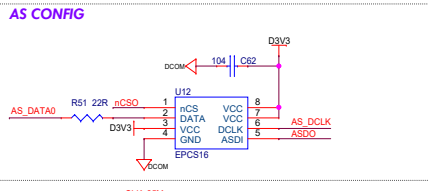
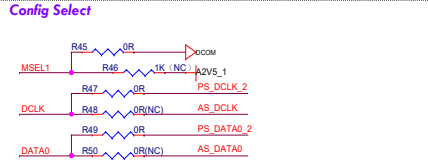
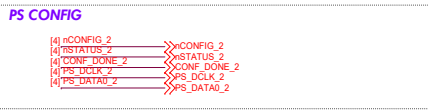
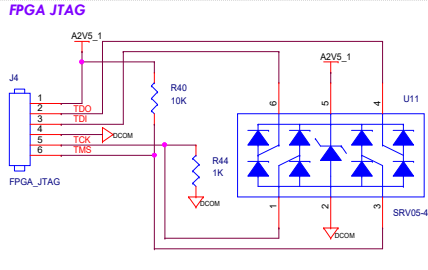
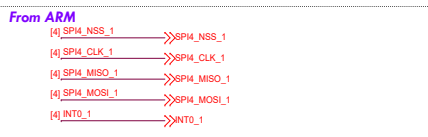




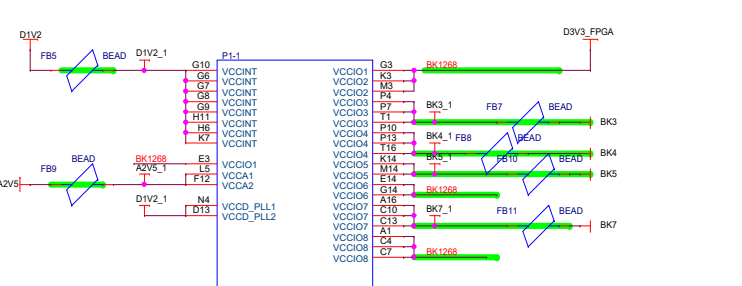
W9825G6JB



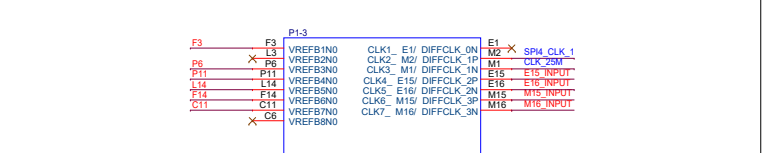
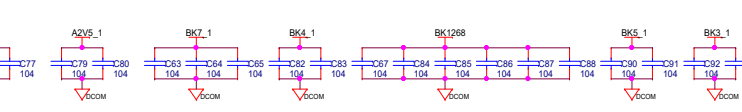
GINGKO Technology. Co., Ltd.		
Title iCore4T		
Size A	Document Number <4>.SDRAM	Rev 0
Date:	Monday, January 13, 2020	Sheet 5 of 9



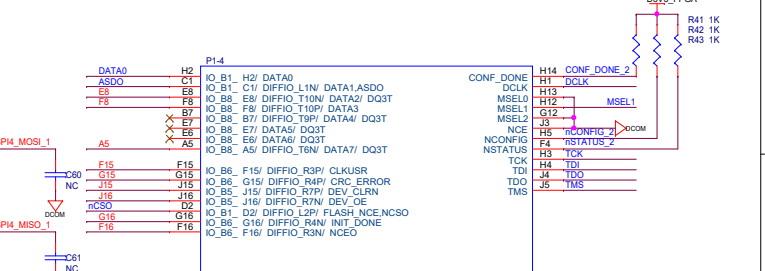
EP4CE10F17C8N
VERSION : 1.0
PAGE : 2 of 5
DATE : March 2010



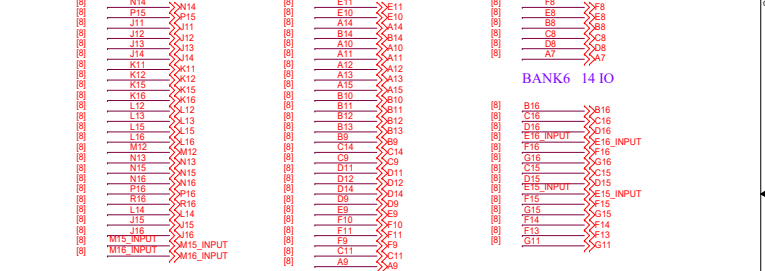
EP4CE10F17C8N
VERSION : 1.0
PAGE : 1 of 5
DATE : March 2010



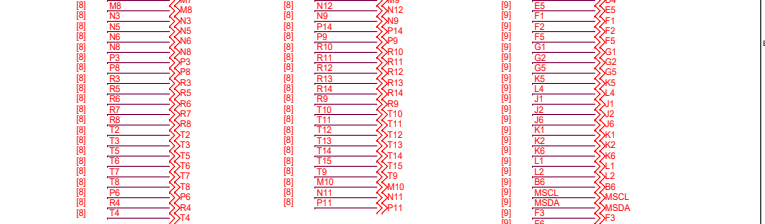
EP4CE10F17C8N
VERSION : 1.0
PAGE : 3 of 5
DATE : March 2010



EP4CE10F17C8N
VERSION : 1.0
PAGE : 4 of 5
DATE : March 2010



EP4CE10F17C8N
VERSION : 1.0
PAGE : 5 of 5
DATE : March 2010



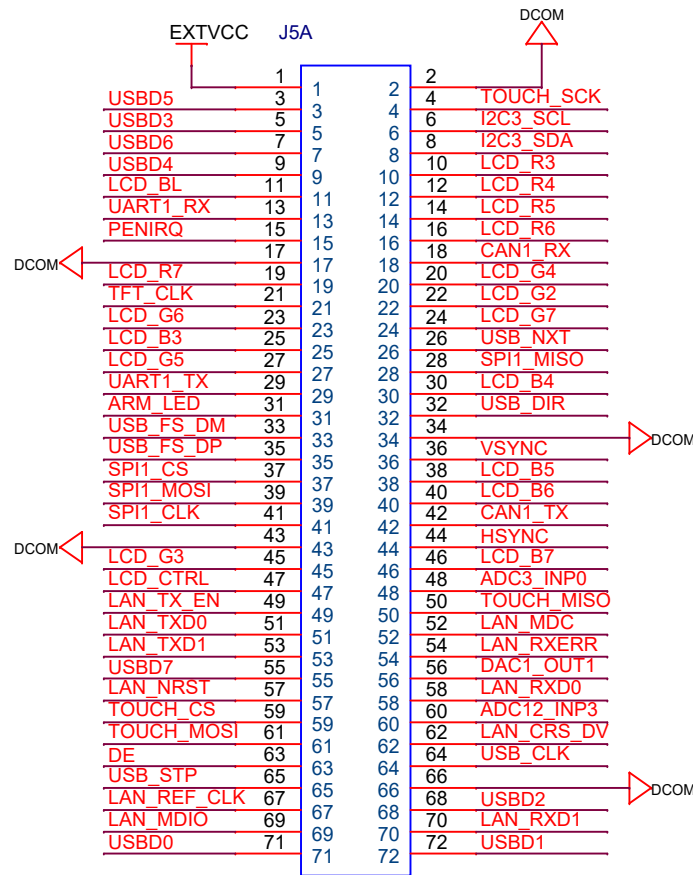
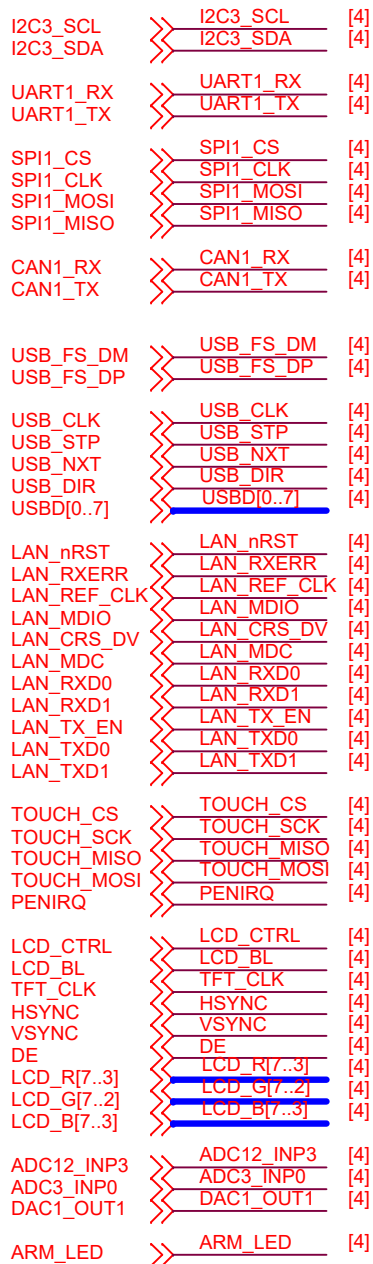
EP4CE10F17C8N
VERSION : 1.0
PAGE : 6 of 5
DATE : March 2010



EP4CE10F17C8N
VERSION : 1.0
PAGE : 7 of 5
DATE : March 2010

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Title	iCore4T	
Size	Document Number	Rev
C	<S>-FPGA	0
Date	Monday, January 13, 2020	Sheet 6 of 9

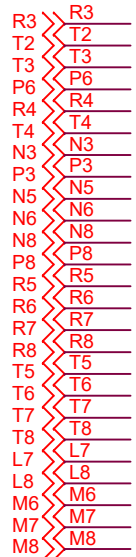


GOLDFINGER

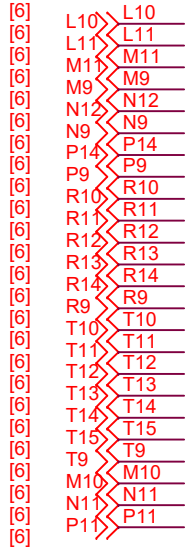
ARM IO 66

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Title		
iCore4T		
Size	Document Number	Rev
A	<6>.Goldfinger_A	0
Date:	Monday, January 13, 2020	Sheet 7 of 9

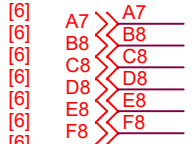
BANK3 25 IO



BANK4 24 IO



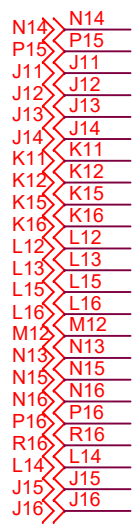
BANK8 6 IO



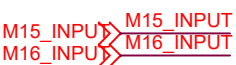
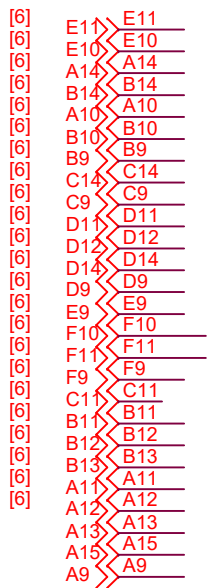
BANK6 14 IO



BANK5 25

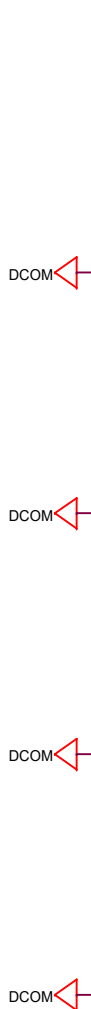


BANK7 26

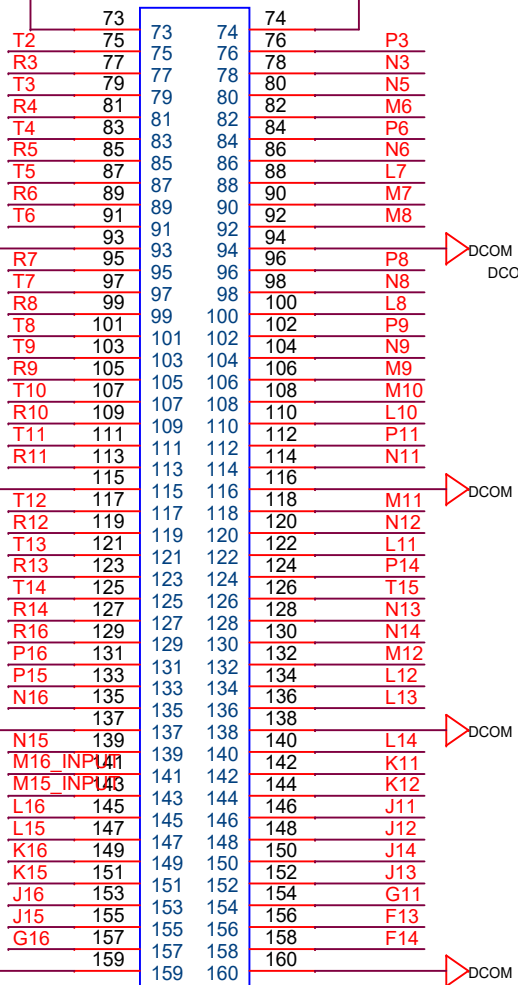


FPGA IO 120

P_OUT

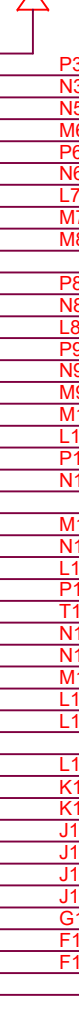


J5B

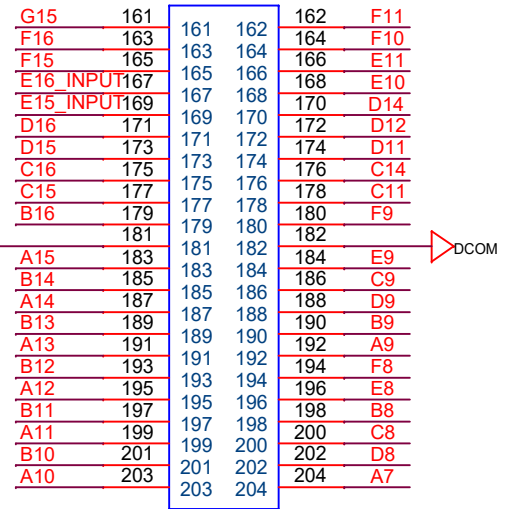


GOLDFINGER

DCOM



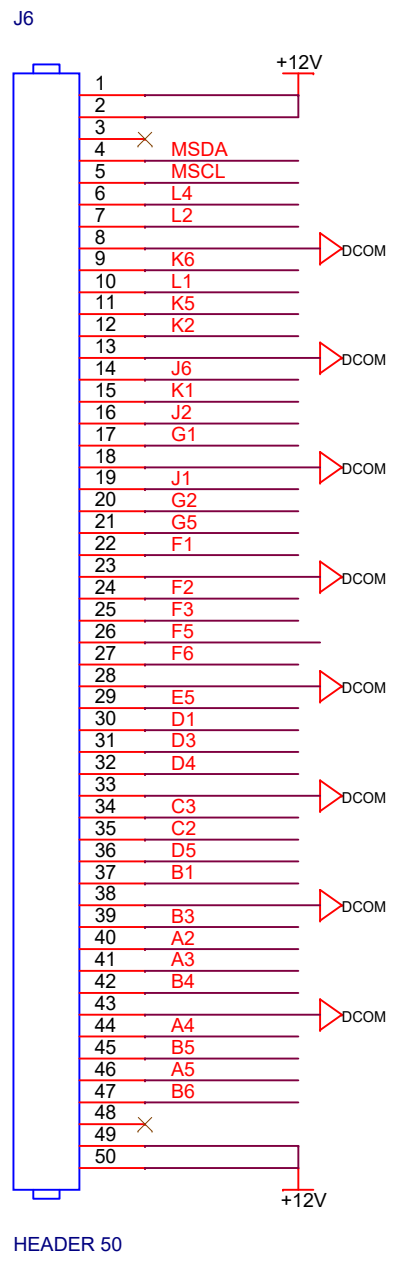
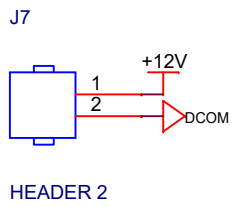
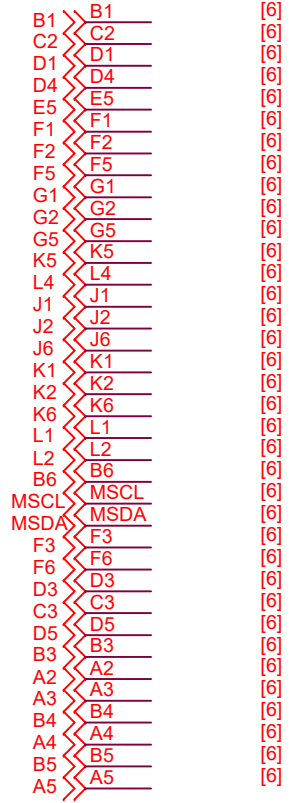
J5C



GOLDFINGER

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Title		
iCore4T		
Size	Document Number	Rev
A	<7>.Goldfinger_B	0
Date:	Monday, January 13, 2020	Sheet 8 of 9

50P 36IO



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Title		
iCore4T		
Size	Document Number	Rev
A	<8>.Connector	<0>
Date:	Monday, January 13, 2020	Sheet 9 of 9